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10/20/2005

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IP DEPARTMENT
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Elizabeth Orleman (Depositor's name)

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11/16/05 (Date)

11/18/2005 MAHME2 00000057 041679 10792120

01 FC:2501 700.00 DA
02 FC:1504 300.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/792,120	03/03/2004	Chung Zen Chen	E1821-00001	3928

TITLE OF INVENTION: BIT SWITCH VOLTAGE DROP COMPENSATION DURING PROGRAMMING IN NONVOLATILE MEMORY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1500 \$700	\$300	\$1800 \$1000	01/20/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
HOANG, HUAN	2827	365-185180

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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1 Duane Morris LLP

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Elite Semiconductor Memory
Technology, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hsinchu, Taiwan R.O.C.

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

☒ Issue Fee☒ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies

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☒ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Joseph A. Powers

Date

11/16/05

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47,006

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